



Form PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTORNEY DOCKET NO.
10030747-1SERIAL NO.
10/600,600







APPLICANT

John F. Casey, et al.

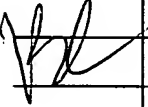
FILING DATE
June 19, 2003GROUP
1762

REFERENCE DESIGNATION

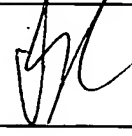
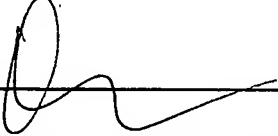
U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME
	4,808,274	02-1989	Nguyen, Pascaline H.
	4,303,480	12-1981	Wood, et al.
	4,806,941	02-1989	Knochel et al.
	6,420,778	07-2002	Sinyansky, V.
	20040023482	02-2004	Dell'Ova et al.
	20010012692	08-2001	Miller et al.

FOREIGN PATENT DOCUMENT

DOCUMENT NUMBER	DATE	NAME	TRANSLATION YES NO
 JP403006818	01-1981	Kiriyama, S.	X

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	Wolf et al., "Silicon Processing for the VLSI Era", Volume 1: Process Technology, Lattice Press, 1986, pages 531-535
EXAMINER 	DATE CONSIDERED 4/27/05

* Copies of these references are not enclosed Pursuant to 37 CFR 1.98(d). (See accompanying IDS)

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